



501.36127CC3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: KOUBUCHI et al

Serial No.: 10/619,039

Filed: July 14, 2003

For: Semiconductor Integrated Circuit Device

Group: 2814

Examiner: L. Pham

SUPPLEMENTAL INFORMATION DISCLOSURE
STATEMENT UNDER 37 CFR 1.97 & 1.98

Mail Stop: DD
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 17, 2006

Sir:

Supplemental to the Information Disclosure Statement filed April 5, 2006,
attached are English translations of the following documents:

JP 02-138757

JP 06-69201

KR 10-1996-0043102 A

KR 10-1994-0009350 B1

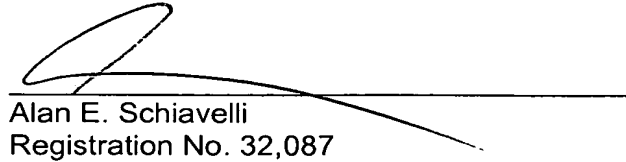
Applicants request that the Examiner consider the Information Disclosure
Statement filed April 5, 2006 and this Supplemental Information Disclosure
Statement.

Please charge any shortage in the fees due in connection with the filing of this
paper, including extension of time fees, to the deposit account of Antonelli, Terry,

Stout & Kraus Deposit Account No. 01-2135 (Case: 501.36127CC3), and please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP



Alan E. Schiavelli
Registration No. 32,087

AES/at
(703) 312-6600
Attachments